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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/484,432	01/18/2000	Muneki Ando	35.C14218	9693
5514 75	90 10/20/2003		EXAM	NER
FITZPATRICK CELLA HARPER & SCINTO			ABDULSELAM, ABBAS I	
30 ROCKEFEL NEW YORK, 1		•	ART UNIT PAPER NUMBER	
,			2674	17
			DATE MAILED: 10/20/2003	' (
	1.7		•	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)
Office Action Summary		09/484,432	ANDO ET AL.
		Examiner	Art Unit
		Abbas I Abdulselam	2674
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the	orrespondence address
THE - Exte after - If the - If NO - Failt - Any	IORTENED STATUTORY PERIOD FOR REPL' MAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. In period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period vare to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be y within the statutory minimum of thirty (30) o will apply and will expire StX (6) MONTHS fro , cause the application to become ABANDO	timely filed days will be considered timely. om the mailing date of this communication. NED (35 U.S.C. § 133).
1)🖂	Responsive to communication(s) filed on 08.	July 2003 .	
2a)⊠	This action is <b>FINAL</b> . 2b)☐ Th	is action is non-final.	
3)□ Disposit	Since this application is in condition for allowa closed in accordance with the practice under ion of Claims		
4)🖂	Claim(s) 61-70 is/are pending in the application	on.	
	4a) Of the above claim(s) is/are withdraw	wn from consideration.	
5)	Claim(s) is/are allowed.		
6)⊠	Claim(s) 61-70 is/are rejected.		
7)	Claim(s) is/are objected to.		•
8) <u>□</u> Applicat	Claim(s) are subject to restriction and/o ion Papers	r election requirement.	
9)	The specification is objected to by the Examine	r.	•
10)	The drawing(s) filed on is/are: a) accep	oted or b) objected to by the Ex	caminer.
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance.	See 37 CFR 1.85(a).
11)	The proposed drawing correction filed on	_ is: a)□ approved b)□ disapp	proved by the Examiner.
	If approved, corrected drawings are required in rep	oly to this Office action.	
12)	The oath or declaration is objected to by the Ex	aminer.	
Priority (	under 35 U.S.C. §§ 119 and 120		
13)[	Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. § 119	(a)-(d) or (f).
a)	☐ All b)☐ Some * c)☐ None of:		
	1. Certified copies of the priority documents	s have been received.	
	2. Certified copies of the priority documents	s have been received in Applica	ation No
• •	3. Copies of the certified copies of the prior application from the International But	reau (PCT Rule 17.2(a)).	_
	See the attached detailed Office action for a list		
•	Acknowledgment is made of a claim for domesti		
	i) $\square$ The translation of the foreign language pro Acknowledgment is made of a claim for domesti		
Attachmen		<u>_</u>	
2) 🔲 Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informa	ary (PTO-413) Paper No(s) al Patent Application (PTO-152)

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#### **DETAILED ACTION**

## Response to Arguments

1. Applicant's arguments filed 07/08/03 have been fully considered but they are not persuasive.

Applicant agues that the horizontal detector (9) shown in Fig. 1 is not connected to and doses not cooperate with any modulator that outputs modulation signal to column wiring. However, Gyouten et al. (USPN 6195077) teach an output control means for adjusting an amount of correction for an output voltage of the segment side circuit. See column 4, lines 42-47. Gyouten teaches a pulse width modulator (203), the pulse width correction (col. 17, lines 51-65) and correction of voltage for the purpose of marinating luminance (col. 17, lines 21-33). Fukuda et al. (5867593) also disclose a luminance level correction-processing unit (14) including a horizontal detector (9) for calculating the difference values between two luminance levels. col. 6, lines 34-40 and Fig. 1. Both Gyouten and Fukuda teach about image data processing and luminance corrections and one of ordinary skill in the art would have looked toward Fukuda Fig. 1 (90) for the manner by which luminance levels are corrected. In addition, Fukuda teaches an image kind discriminating and identification unit in order that the required images are outputted. See column 2, lines 52-67.

### Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person

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having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 61-70 are rejected under 35 U.S.C. 103(a) as being unpatentable over Gyouten et al. (USPN 6195077) in view of Fukuda et al. (USPN 5867593).

Regarding claims 61 and 66, Gyouten teaches a liquid crystal panel (101) with segment drive circuit (102), and side drive circuit (103) which is used for selecting sequentially to drive scanning lines. Gyouten teaches displaying images in a simple matrix type which displays an image with a pixel located at each intersections of the electrodes (X1, Y1), (X2, Y2), (X3, Y3)......(Xm, Yn). See column 11, lines 47-58, Fig 1 and Fig 39. Gyouten teaches an output control means for adjusting an amount of correction for the output voltage of the segment side circuit according to the distance between an arrangement position of the segment drive circuit and a position of scanning line selected by the side drive circuit in the liquid crystal panel. See column 1, lines 11-14, column 4, lines 42-47, and Fig 39. Moreover, Gyouten teaches correction clock generator circuit (70) in conjunction with the correction base clock for indicating the position where a correction period is to be provided, and the length of correction period is adjusted by the correction clock generator circuit. In addition, Gyouten teaches counter (72) changes in the outputs (B1, B2, B3) to high level; and further teaches the display data stored in the line latch (123) of the drive circuit (102) that would be given to the liquid crystal drive output circuit (126). See column 1, lines 55-63 and Fig 41. Gyouten also teaches maintaining uniformity of luminance as well as the voltage waveforms with the correction voltage changes. See column 17, lines 30-33, lines 49-65 and Fig 20. However, Gyouten does not teach a correction circuit

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such that the correction pulse is adjusted according to the difference between luminance of the signals for pixels that are adjacent to each other in the row direction. Fukuda on the other hand teaches gradient vector direction unit (16) and luminance level correction processing unit (14) including horizontal difference detector (9) calculating the difference value between the luminance levels of an arbitrary pixel and a pixel adjacent in the horizontal direction. See col. 6, lines 34-40 and Fig 1.

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Therefore, it would have been obvious to one having skill in the art at the time the invention was made to modify Gyouten's liquid crystal display panel to include Fukuda's luminance correction technique including horizontal difference detector. One would have been motivated in view of the suggestion in Fukuda that the luminance level correction process including the horizontal difference level is functionally equivalent to the desired adjustment based on the difference between luminance levels of adjacent pixels. The use of luminance level correction processing unit helps function a display system with image forming technique as taught by Fukuda.

Regarding claims 63 and 68-70, Gyouten teaches the pulse width modulator (203), and correction clock with modulator (204), which is, supplied with reference correction clock signals. See Fig 29. Gyouten also teaches changing of the length of correction period. See column 16, lines 5-11 and Fig 14.

Regarding claims 62 and 67, Gyouten teaches the liquid crystal panel (101) with common electrodes, segment electrodes and liquid crystal layer interposed between electrodes. Column 2, lines 9-12. In addition, it is well known in the art and would be obvious to utilize a display panel

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composed of electron emission devices with a phosphor layer. Gyouten also teaches improving display in liquid crystal device apparatus. See column 1, lines 7-10

Regarding claims 64-65 Gyouten teaches amount of correction with respect to uniformly luminance waveforms. See column 17, lines 21-23 and Fig 18.

#### Conclusion

3. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

4. Any inquiry concerning this communication or earlier communication from the examiner should be directed to **Abbas Abdulselam** whose telephone number is (703) 305-8591. The examiner can normally be reached on Monday through Friday (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard Hjerpe**, can be reached at (703) 305-4709.

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Any response to this action should be mailed to:

Commissioner of patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314

Hand delivered responses should be brought to crustal park II, Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology center 2600 customer Service office whose telephone number is (703) 306-0377.

Abbas Abdulselam

Examiner

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October 4, 2003

SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2600